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TO:

Examiner: JCWS
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

FROM: Douglas P. Mueller

OUR REF: 10921.0313USWO

TELEPHONE: (612) 455.3800

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Title of Document: **Request for Corrected Filing Receipt, Marked-up copy of the Filing Receipt**Applicant: TSUKADA et al.
Serial No.: 10/533,489
App. Filed: April 28, 2005
Group Art No.: 2812

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S/N 10/533,489

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: TSUKADA et al.
Serial No.: 10/533,489 Group Art Unit: 2812
Filed: April 28, 2005 Docket No.: 10921.0313USWO
Title: CHIP RESISTOR, PROCESS FOR PRODUCING THE SAME, AND
FRAME FOR USE THEREIN

CERTIFICATE UNDER 37 CFR 1.6(d)

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Name: Ralynn Wilhelm

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Dear Commissioner:

Enclosed is a photocopy of the filing receipt from the United States Patent and
Trademark Office in the above-identified application showing requested corrections.

The Applicant's name is incorrect.

Please replace [TSUKDA] with TSUKADA.

Correction of the records of the United States Patent and Trademark Office and issuance
of a corrected filing receipt are respectfully solicited.

Respectfully submitted,



Hamre, Schumann, Mueller & Larson, P.C.
225 South Sixth Street, Suite 2650
Minneapolis, MN 55402
(612)-455-3800

Dated: March 17, 2006

By 

Douglas P. Mueller
Reg. No. 30,300

DPM/rkw

Page 1 of 3



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/533,489	04/28/2005	2812	2750	10921.313USWO	1	33	9

CONFIRMATION NO. 4098

52835
HAMRE, SCHUMANN, MUELLER & LARSON, P.C.
P.O. BOX 2902-0902
MINNEAPOLIS, MN 55402

FILING RECEIPT



OC000000017962827

Date Mailed: 02/09/2006

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Applicant(s)

TSUKADA

Torayuki [Tsukda] Kyoto, JAPAN;
Tadatoshi Miwa, Kyoto, JAPAN;

Assignment For Published Patent Application

ROHM CO., LTD., Kyoto-shi, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 62835.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/13964 10/30/2003

Foreign Applications

JAPAN 2002-318648 10/31/2002
JAPAN 2002-348883 11/29/2002
JAPAN 2002-348884 11/29/2002
JAPAN 2002-353514 12/05/2002

Projected Publication Date: 05/11/2006

Non-Publication Request: No

Early Publication Request: No

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Title

Chip resistor, process for producing the same, and frame for use therein

Preliminary Class

438

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